

REPLACEMENT SHEET



FIG. 14 RELATED ART

Step	Processing
step0	Third pointer 60 ← address information
step1	r1 ← data storage means 10 (at address designated by first pointer 30) First pointer 30 ← first pointer 30 + 1
step2	Setting of the number of repetitions
step3	r0 ← setting of flag of latch 240 in specific bit of 1-word data stored in data storage means 10 (at address designated by third pointer 60) Second pointer 22 ← second pointer 22 + 1
step4	Data storage means 10 (at address designated by third pointer 60) ← r0
step5	r1 ← shifting of data in r1 to higher-order bits by one bit Third pointer 60 ← address information Return to step 3
step6	Return to step 1

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FIG. 13

RELATED ART

